Lesson Plan for semester 1(FOR MINOR COURSE & 3 YEAR MDC)

ELT-MD-CC-1-1-TH

Course Name: Fundamentals of Circuit Theory and Electronic Device

UNIT	TOPIC	NO. OF CLASSES	TIME PERIOD	TEACHER'S NAME
I	CIRCUIT ELEMENTS	12	SEP - NOV	TG
	CIRCUIT ANALYSIS			
	DC & AC ANALYSIS			
	NETWORK THEOREM			
II	SEMICONDUCTOR BASICS	11	NOV- JAN	TG
	JUNCTION DIODE & ITS APPLICATION			
III	BJT	11	SEP - NOV	AS
	TRANSISTOR BIASING			
IV	BJT AMPLIFIER	11	NOV- JAN	AS
	FET			

ELT-MD-CC-1-1-P

Course Name: Fundamentals of Circuit Theory and Electronic Device LAB

SL NO.	TOPIC	TIME PERIOD
1	To Familiarize with Basic Electronic Components (R, C, L, Diodes, Transistors), Digital Multimeter, Function Generator and Oscilloscope.	SEP - NOV
2	Verification of (a) Thevenin's Theorem and (b) Norton's Theorem	SEP - NOV
3	Verification of (a) Superposition Theorem and (b) Maximum Power Transfer Theorem.	SEP - NOV
4	Study of the I-V Characteristics of (a) P-N Junction Diode and (b) Zener Diode	SEP - NOV

5	Study of (a) Half Wave rectifier and (b) Full Wave rectifier (FWR) without and with Capacitor Filter.	NOV- JAN
6	Study of Zener Diode as Voltage Regulator and its Load Regulation.	NOV- JAN
7	Study of the I-V Characteristics of the Common Emitter Configuration of BJT	NOV- JAN
8	Study of the I-V Characteristics of the Common Base Configuration of BJT	NOV- JAN
9	Study of the I-V Characteristics of JFET.	NOV- JAN

Signature
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H.O.D of Electronics